

# Logic And Architecture Synthesis: State-of-the-art And Novel Approaches IFIP Workshop On Logic And Architecture Synthesis, 1994

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Gabriale Saucier; Anne Mignotte

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Full Title: Logic And Architecture Synthesis: State-of-the-art And Novel Approaches IFIP Workshop On Logic And Architecture Synthesis, 1994. Author/Editor(s): ... Functional multiple-output decomposition with application to . An overview on Decision Diagrams (DDs), the state- of-the-art data structure in computer aided design of integrated . several problems in the area of logic synthesis. The most ..... IFIP. Workshop on Logic and Architecture Synthesis, Grenoble, pages 71{80, 1994. .... and A. Mignotte, editors, Novel Approaches in. Logic and ... 3rd International Workshop on Applications of the Reed-Muller Expansion in . In: Logic and Architecture Synthesis - State-of-the-art and novel approaches, .... Proc. of the FPGA94, 1994 ACM/SIGDA Second International Workshop on .... IFIP 10.5 Workshop on Application of the Reed-Muller expansion in Circuit Design Veröffentlichungen des Instituts für Informatik der Universität Bonn . Introduction to the special issue on the 36th European Solid-State Circuits Conference . Journal of Systems Architecture : the EUROMICRO journal, 56(7), 265-277. .... Arts, H.M.A.M., Berkelaar, M.R.C.M. & Eijk, C.A.J. van (1998). ..... and Novel Approaches : IFIP Workshop on Logic and Architecture Synthesis, 1994. (pp. Jacobi Ricardo - UCL - Publication de recherche (Détail) Logic and architecture synthesis : state-of-the-art and novel approaches : IFIP Workshop on Logic and Architecture Synthesis, 1994 / edited by Gabriële Saucier . Logic and architecture synthesis : state-of-the-art and novel . In ``Proc. of the 8th International Conference and Workshop on Database and .... Research Report 85110-CS, Universität Bonn, 1994. .... ``In Logic and Architecture Synthesis, State-of-the-art and Novel Approaches, pp. .... In ``Proceedings of PROLAMAT 98 - The Tenth International IFIP WG 5.2/5.3 Conference,, 09. USC Viterbi School of Engineering: Publications 4.2 Sequential Logic Synthesis (1984 - 2002) .... G.D. Hachtel, R.K. Brayton and F.G. Gustavson, Sparse Tableau Approach to Nonlinear .... Proceedings of the 1981 ECCTD, La Hague, Invited Paper, state-of-the-art review lecture. .... Table Look Up Architectures, 2nd International Workshop on Field Programmable Logic ... Maciej Ciesielski - University of Massachusetts Amherst 27 Jul 1995 . Buy Logic and Architecture Synthesis by Gabriele Saucier with free worldwide delivery ... State-of-the-art and Novel Approaches - IFIP Workshop on Logic and ... Processing and held in Grenoble, France, in December 1994. publications 1961-2002 - Electrical Engineering & Computer Sciences Our approach is based on novel operating system components and a novel . systems our prototype implementation outperforms the state-of-the-art model ..... 1994 IFIP Workshop on Logic and Architecture Synthesis, Grenoble, Seiten: 61 - 70 Logic and Architecture Synthesis : State-of-the-art and Novel . Generation of fault tests for linear logic networks, IEEE Trans. on Computers, vol. .... A survey of the state of the art of design automation, Computer, vol. .... IFIP Workshop on Knowledge Based Systems for Test and Diagnosis, .... A unified approach for the synthesis of scalable and testable embedded architectures, Proc. 0412726904 - Logic and Architecture Synthesis Ifip Advances in . 32-40, 1994 . Automatic synthesis of low-power gated-clock Finite-State Machines. ... L. Benini, P. Vuillod, G. De Micheli, Iterative re-mapping for logic circuits, IEEE .... ACM/EUROMICRO JOURNAL OF SYSTEMS ARCHITECTURE: vol. .... L. Benini, M. Favalli, P. Olivo, B. Riccò, A novel approach to cost effective estimate ... Silicon - ?????? ?????? Logic

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